

# SPECIFICATION

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## **[METHOD FOR DEPOSITING THIN FILM USING PLASMA CHEMICAL VAPOR DEPOSITION]**

### Background of Invention

[0001] Field of the Invention

[0002] The present invention relates to a deposition process for a semiconductor device. More particularly, the present invention relates to a method for depositing thin film using plasma enhanced chemical vapor deposition.

[0003] Description of Related Art

[0004] Currently, chemical vapor deposition is the most important process in semiconductor manufacturing for depositing a thin film. In any semiconductor device where a thin film is required, whether the thin film is conductive, semi-conductive or dielectrics, the thin film is formed by chemical vapor deposition. Chemical vapor deposition is defined as the formation of a thin film by the reaction of vapor-phase chemicals that contain the required constituents. Thus, the thin film formed by such a method is superior in crystallinity, stoichiometry and other relevant material properties.

[0005] To lower the required reaction temperature so as to reduce the thermal budget of the process, plasma enhanced chemical vapor deposition gradually becomes one of the major thin film deposition processes. Normally, in a PECVD chamber, the deposition of a thin film on a substrate includes performing the plasma enhanced chemical vapor deposition process, followed by cleaning the substrate-free chamber. A pre-deposition process is further performed to isolate contaminants generated in the cleaning of the substrate-free chamber. A next deposition process is then

performed.

[0006] However, as the thin film that is being deposited on the substrate is an insulating material layer or a high resistance thin film, such as, intrinsic amorphous silicon, a large quantity of charges is accumulated on the surface of the electrode in the PECVD chamber. If thin film deposition is continued for another batch of substrate, the distribution of plasma is affected by the uneven distribution of the accumulated charges, leading to a non-uniform thickness of the depositing thin film.

## Summary of Invention

[0007] Accordingly, the present invention provides a method for depositing thin film using plasma enhanced chemical vapor deposition, wherein the accumulated charges on the surface of the electrode in the chamber is reduced.

[0008] The present invention also provides a method for depositing thin film using plasma enhanced chemical vapor deposition, wherein the distribution of plasma being affected the uneven distribution of the accumulated charges is prevented.

[0009] The present invention further provides a method for depositing thin film using plasma enhanced chemical vapor deposition, wherein the uniformity of the thickness of the depositing thin film is improved.

[0010] Accordingly, the present invention provides a method for depositing thin film using plasma enhanced chemical vapor deposition, wherein plasma is used to enhance the chemical reaction in a PECVD chamber to form a thin film on a substrate. The substrate is then removed from the chamber. A cleaning gas is then passed into the chamber to remove residues in the chamber. A pre-deposition process is then conducted to isolate contaminants from the cleaning process before loading another substrate in the chamber. A discharge plasma treatment is further conducted, followed by loading another batch of substrate for thin film deposition by PECVD.

[0011] According to the present invention, a discharge plasma treatment is used to drastically lower the accumulated electrical charges on the electrode surface in the chamber subsequent to the pre-deposition process in order to improve the uniformity of the thickness of the deposited thin film.

[0012] It is to be understood that both the foregoing general description and the following detailed description are exemplary, and are intended to provide further explanation of the invention as claimed.

## Brief Description of Drawings

[0013] The accompanying drawings are included to provide a further understanding of the invention, and are incorporated in and constitute a part of this specification. The drawings illustrate embodiments of the invention and, together with the description, serve to explain the principles of the invention. In the drawings,

[0014] Figure 1 is a flow diagram illustrating a method of thin film deposition using plasma enhanced chemical vapor deposition according to an embodiment of the present invention.

## Detailed Description

[0015] The present invention employs plasma enhanced chemical vapor deposition to deposit a thin film. Particularly, a discharge plasma treatment is performed after the pre-deposition process is conducted. The amount of the accumulated charges on the electrode surface in the chamber is greatly reduced to minimize the effects of the accumulated charges. The uniformity of the thickness of the thin film is thereby improved. The present invention is generally applicable in semiconductor processing, as illustrated in the following embodiment.

[0016] Figure 1 is a flow diagram illustrating a method of thin film deposition using plasma enhanced chemical vapor deposition according to an embodiment of the present invention.

[0017] Referring to Figure 1, in step 100, a plasma enhanced chemical vapor deposition process is performed to form a thin film on a substrate. Chemical reaction is enhanced by plasma to form a layer of thin film on the substrate that is placed in the PECVD chamber.

[0018] Thereafter, in step 110, the substrate is removed from the chamber, wherein the substrate is removed from the PECVD chamber under vacuum.

[0019] Continuing to step 120, a cleaning process is performed. A cleaning gas is passed into the chamber to remove residues using plasma enhanced chemical reaction to mitigate the problem with particles. The cleaning gas includes a fluorine-based gas, for example,  $\text{NF}_3$ .

[0020] In step 130, a pre-deposition process is performed. The deposition process is performed under a substrate-free condition to isolate contaminants, for example,  $\text{NF}_3$ , generated from the cleaning process.

[0021] In step 140, a discharge plasma treatment is conducted, especially when the depositing thin film is an insulating material or a high resistance thin film, such as, intrinsic amorphous silicon, where a large quantity of charges is accumulated on the electrode surface in the chamber, causing an uneven distribution of plasma due to the uneven distribution of the accumulated charges. The present invention, therefore, further performs a discharge plasma treatment subsequent to the pre-deposition process. The gas used in the discharge plasma treatment is selected from the group of gases consisting of  $\text{H}_2$ ,  $\text{N}_2$ , Ar, He and a mixture of the above gases.

[0022] Subsequently, in step 150, another batch of substrate is loaded into the chamber and step 100 is repeated to perform the thin film deposition.

[0023] Accordingly, after the pre-deposition process of the present invention, a discharge plasma treatment is performed to reduce the accumulated charges on the electrode surface in the chamber, especially the depositing thin film is an insulation material or a high resistance thin film.

[0024] Since the present invention can lower the amount of the accumulated charges, the effect of the accumulated charges is minimized.

[0025] Further, the present invention employs a discharge plasma treatment to lower the amount of the accumulated charges, the uniformity of the thickness of the depositing thin film is thereby improved.

[0026] It will be apparent to those skilled in the art that various modifications and variations can be made to the structure of the present invention without departing from the scope or spirit of the invention. In view of the foregoing, it is intended that

the present invention cover modifications and variations of this invention provided they fall within the scope of the following claims and their equivalents.